

SEMiX[®] 5

3-Level NPC IGBT-Module

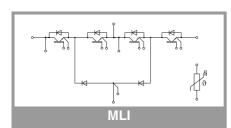
Engineering Sample SEMiX305MLI12E4

Target Data

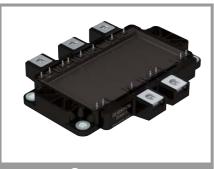
Features

- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

- Case temperature limited to T_C=125°C max.
- Product reliability results are valid for $T_{jop}{=}150^{\circ}C$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- Diode5: clamping diodes D5 & D6
 For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"



Absolute	e Maximum Ratii	ngs			
Symbol	Conditions		Values	Unit	
IGBT1					
V _{CES}	T _j = 25 °C		1200	V	
lc		T _c = 25 °C	451	Α	
	T _j = 175 °C	T _c = 80 °C	347	Α	
I _{Cnom}			300	A	
I _{CRM}	I _{CRM} = 3 x I _{Cnom}		900	А	
V _{GES}			-20 20	V	
t _{psc}	V _{CC} = 800 V, V _G V _{CES} ≤1200 V	_E ≤ 15 V, T _j = 150 °C,	10	μs	
Tj			-40 175	°C	
IGBT2					
V _{CES}	T _j = 25 °C		1200	V	
I _C	T _i = 175 °C	T _c = 25 °C	451	A	
	$i_j = 175$ C	T _c = 80 °C	347	А	
I _{Cnom}			300	А	
I _{CRM}	I _{CRM} = 3 x I _{Cnom}		900	A	
V _{GES}			-20 20	V	
t _{psc}	V _{CC} = 800 V, V _G V _{CES} ≤ 1200 V	$E \le 15 \text{ V}, \text{ T}_{j} = 150 \text{ °C},$	10	μs	
Tj			-40 175	°C	
Diode1					
V _{RRM}	T _j = 25 °C		1200	V	
l _F	T _i = 175 °C	T _c = 25 °C	344	А	
	$=1_{j}=175$ C	T _c = 80 °C	257	A	
I _{Fnom}			300	А	
I _{FRM}	$I_{FRM} = 2 \times I_{Fnom}$		600	А	
I _{FSM}	10 ms, sin 180°, T _j = 25 °C		1620	А	
Tj			-40 175	°C	
Diode2					
V _{RRM}	T _j = 25 °C		1200	V	
I _F	T _i = 175 °C	T _c = 25 °C	344	A	
	$i_j = 175$ C	T _c = 80 °C	257	А	
I _{Fnom}			300	А	
I _{FRM}	$I_{FRM} = 2 \times I_{Fnom}$		600	А	
I _{FSM}	10 ms, sin 180°,	T _j = 25 °C	1620	А	
Tj			-40 175		
Diode5					
V _{RRM}	T _j = 25 °C		1200	V	
l _F	T _i = 175 °C	T _c = 25 °C	344	Α	
	$ _{j} = 1/5 $ C	T _c = 80 °C	257	А	
I _{Fnom}			300	А	
I _{FRM}	I _{FRM} = 2 x I _{Fnom} 10 ms, sin 180°, T _j = 25 °C		600	А	
I _{FSM}			1620	А	
Tj			-40 175	°C	
Module	•	I			
I _{t(RMS)}			340	Α	
T _{stg}	module without	TIM	-40 125	°C	
V _{isol}	AC sinus 50Hz,		4000	V	



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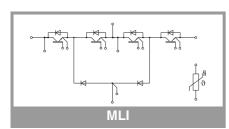
Engineering Sample SEMiX305MLI12E4

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Characte	1					
Symbol	Conditions		min.	typ.	max.	Uni
IGBT1						
V _{CE(sat)}	I _C = 300 A	T _j = 25 °C		1.80	2.05	V
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.20	2.40	V
V _{CE0}	chiplevel	T _i = 25 °C		0.80	0.90	v
▼ CE0	chiplevel	$T_{i} = 150 \text{ °C}$		0.70	0.80	v
ror	V _{GE} = 15 V	$T_i = 25 °C$		3.3	3.8	mΩ
r _{CE}	chiplevel	$T_i = 150 ^{\circ}C$		5.0	5.3	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_C = 12 \text{ n}$,	5	5.8	6.5	V
	$V_{GE} = 0 V, V_{CE} = 12$	J	5.0	4	mA	
I _{CES} C _{ies}	$\mathbf{v}_{GE} = 0 \mathbf{v}, \mathbf{v}_{CE} = 1\mathbf{z}$	f = 1 MHz		18.6	4	nF
	V _{CE} = 25 V	f = 1 MHz		1.16		nF
Coes	V _{GE} = 0 V	f = 1 MHz				
Cres				1.02		nF
Q _G	V _{GE} = - 8 V+ 15 V			1700		nC
R _{Gint}	$T_j = 25 \ ^{\circ}C$ $V_{CC} = 600 \ V$			2.5		Ω
t _{d(on)}	$v_{\rm CC} = 600 v$ $I_{\rm C} = 300 \text{ A}$	T _j = 150 °C		71		ns
t _r	$-V_{GE} = +15/-8 V$	T _j = 150 °C		51		ns
Eon	$R_{G \text{ on}} = 0.5 \Omega$	T _j = 150 °C		17.4		mJ
t _{d(off)}	$R_{G off} = 1.5 \Omega$	T _j = 150 °C		488		ns
t _f	$di/dt_{on} = 5700 \text{ A/}\mu\text{s}$	T _j = 150 °C		148		ns
E _{off}	di/dt _{off} = 2300 A/µs du/dt = 3500 V/µs	T _j = 150 °C		38.7		mJ
R _{th(j-c)}	per IGBT				0.1	K/V
R _{th(c-s)}	per IGBT (λgrease=	=0.81 W/(m*K))		0.077	-	K/V
R _{th(c-s)}	per IGBT, pre-applied phase change material			0.037		K/V
IGBT2						
V _{CE(sat)}	I _C = 300 A	T _i = 25 °C		1.80	2.05	V
• CE(sal)	V _{GE} = 15 V	-				-
	chiplevel	T _j = 150 °C		2.20	2.40	V
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V
		T _j = 150 °C		0.70	0.80	V
r _{CE}	$V_{GE} = 15 V$	T _j = 25 °C		3.3	3.8	mΩ
	chiplevel	T _j = 150 °C		5.0	5.3	mΩ
V _{GE(th)}	$V_{GE} = V_{CE}, I_{C} = 12 \text{ n}$	nA	5	5.8	6.5	V
I _{CES}	$V_{GE} = 0 V, V_{CE} = 12$	00 V, T _j = 25 °C			4	mA
Cies		f = 1 MHz		18.6		nF
C _{oes}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		1.16		nF
C _{res}	GE - U V	f = 1 MHz	İ	1.02		nF
Q _G	V _{GE} = - 8 V+ 15 V	1		1700		nC
R _{Gint}	$T_j = 25 °C$			2.5		Ω
t _{d(on)}	V _{CC} = 600 V	T _i = 150 °C		116		ns
t _r	I _C = 300 A	T _i = 150 °C		58		ns
E _{on}	V _{GE} = +15/-8 V	T _i = 150 °C		17.6		mJ
t _{d(off)}	$R_{G \text{ on}} = 0.5 \Omega$ $R_{G \text{ off}} = 1.5 \Omega$	$T_i = 150 ^{\circ}C$		520		ns
t _f	$h_{G off} = 1.5 \Omega$ di/dt _{on} = 4500 A/µs	,		158		ns
4	$di/dt_{off} = 2100 \text{ A/}\mu\text{s}$.,		100		115
E _{off}	du/dt = 4000 V/µs	T _j = 150 °C		40.6		m
R _{th(j-c)}	per IGBT				0.1	K/V
R _{th(c-s)}	per IGBT (λgrease=	=0.81 W/(m*K))		0.09		K/V
R _{th(c-s)}	per IGBT, pre-appli material		0.047		K/V	



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Engineering Sample SEMiX305MLI12E4

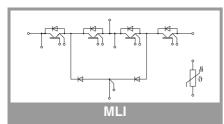
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 For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"

o	eristics					
Symbol	Conditions		min.	typ.	max.	Un
Diode1						
$V_F = V_{EC}$	$I_{\rm F} = 300 \rm{A}$	T _j = 25 °C		2.14	2.46	V
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.07	2.38	V
V _{F0}		T _i = 25 °C		1.30	1.50	v
	chiplevel	T _i = 150 °C		0.90	1.10	V
r _F	- historial	T _j = 25 °C		2.8	3.2	m
	- chiplevel	T _j = 150 °C		3.9	4.3	m
I _{RRM}	I _F = 300 A	T _j = 150 °C		306		А
Q _{rr}	$di/dt_{off} = 4500 \text{ A/}\mu\text{s}$	T _j = 150 °C		46		μ
E _{rr}	V _{CC} = 600 V V _{GE} = +15/-8 V	T _j = 150 °C		22		m
R _{th(j-c)}	per diode	I			0.18	K/
R _{th(c-s)}	per diode (λgrease=0.81 W/(m*K)) 0.074			K/		
R _{th(c-s)}	per diode, pre-applied phase change material			0.058		К/
Diode2						
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		2.14	2.46	V
	$V_{GE} = 0 V$	T _i = 150 °C		2.07	2.38	V
V	chiplevel	$T_i = 25 °C$				
V _{F0}	chiplevel	$T_j = 25 \text{ C}$ $T_i = 150 \text{ °C}$		1.30 0.90	1.50	
r_		$T_{i} = 150 \text{ C}$ $T_{i} = 25 \text{ °C}$		2.8	3.2	m
۲ _F	chiplevel	$T_{i} = 150 \text{ °C}$		3.9	4.3	m
	I _F = 300 A	$T_{i} = 150 \text{ °C}$		306	4.3	A
I _{RRM} Q _{rr}	$di/dt_{off} = 6000 \text{ A/}\mu\text{s}$	$T_{i} = 150 \text{ °C}$		46		μ
Grr	V _R = 600 V	1]= 100 0		-10		μ.
E _{rr}	V _{GE} = +15/-8 V	T _j = 150 °C		-		m
R _{th(j-c)}	per diode				0.18	K/
R _{th(c-s)}	per diode (λgrease			0.098		K/
R _{th(c-s)}	per diode, pre-appl material	ied phase change		0.054		K/
Diode5						
$V_F = V_{EC}$	I _F = 300 A	T _j = 25 °C		2.14	2.46	V
	chiplevel	T _j = 150 °C		2.07	2.38	V
V _{F0}		T _i = 25 °C		1.30	1.50	V
10	- chiplevel	T _i = 150 °C		0.90	1.10	V
ŕ _F	_ chiplevel	T _i = 25 °C		2.8	3.2	m
·F		T _i = 150 °C		3.9	4.3	m
I _{RRM}	I _F = 300 A	T _i = 150 °C		406		A
Q _{rr}	$di/dt_{off} = 5700 \text{ A/}\mu\text{s}$	T _j = 150 °C		46		μ
Err	– V _R = 600 V V _{GE} = +15/-8 V	T _j = 150 °C		24.2		m
R _{th(j-c)}	per diode				0.18	K/
R _{th(c-s)}	per diode (λgrease=0.81 W/(m*K))			0.109		K/
R _{th(c-s)}	per diode, pre-applied phase change material			0.081		K/





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Engineering Sample SEMiX305MLI12E4

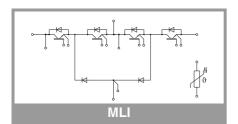
Target Data

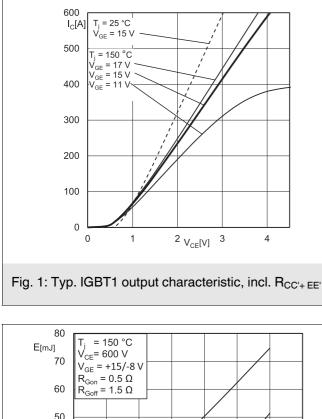
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Characte	ristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Module						
L _{sCE1}				27		nH
L_{sCE2}				34		nH
R _{CC'+EE'}	measured	T _C = 25 °C	0.8			mΩ
	between terminal 5 and 1	T _C = 125 °C	1.1			mΩ
R _{th(c-s)1}	calculated without thermal coupling		0.009			K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module (λ_{grease} =0.81 W/ (m*K))		0.0143		K/W	
R _{th(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.0084		K/W
Ms	to heat sink (M5)		3		6	Nm
Mt		to terminals (M6)	3		6	Nm
						Nm
w				398		g
Temperat	ure Sensor					•
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)			493 ± 5%		Ω
B _{100/125}	R _(T) =R ₁₀₀ exp[B _{100/125} (1/T-1/T ₁₀₀)]; T[K];		3550 ±2%			к





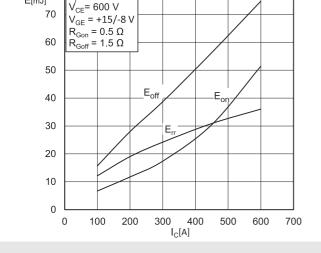
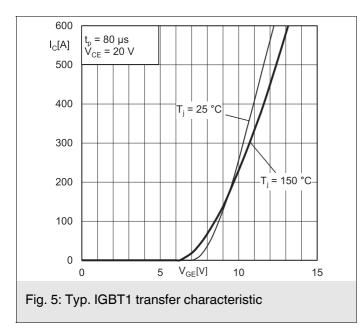
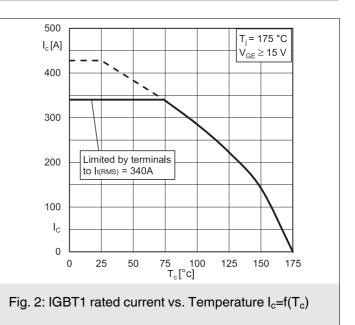
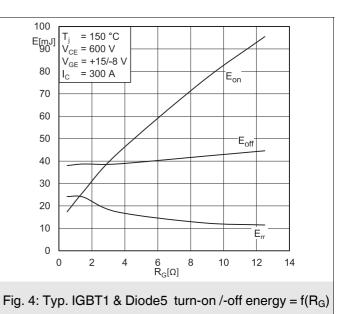
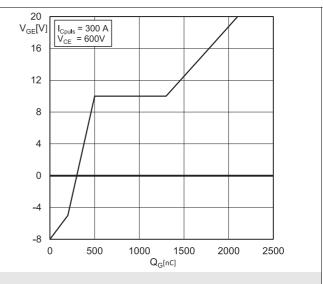


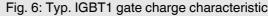
Fig. 3: Typ. IGBT1 & Diode5 turn-on /-off energy = f (I_C)

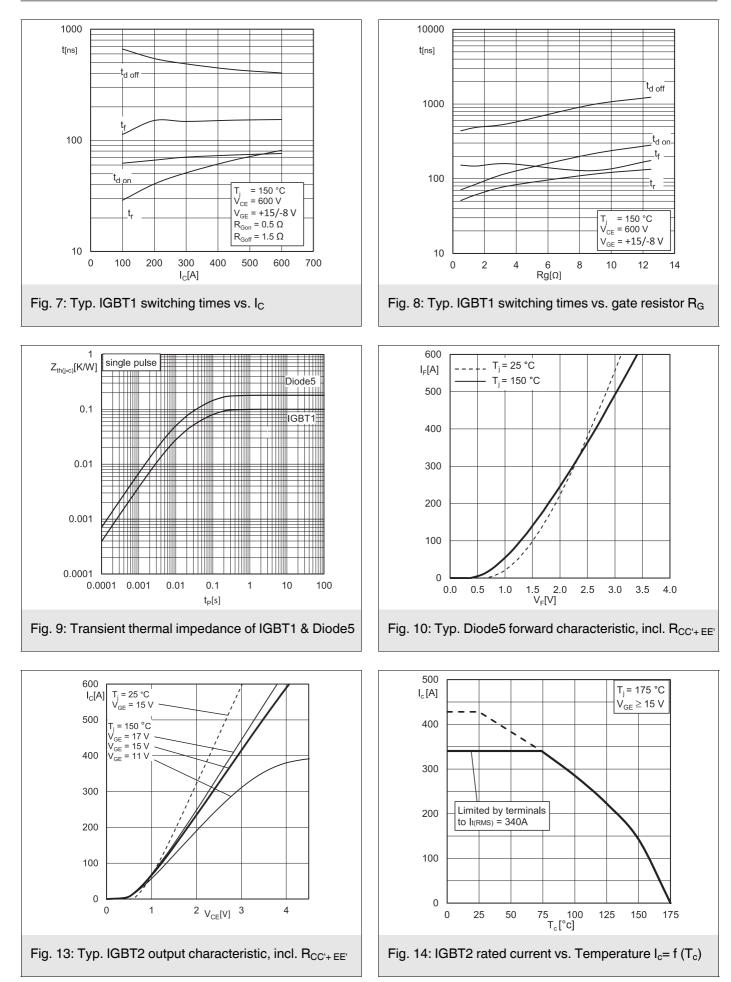




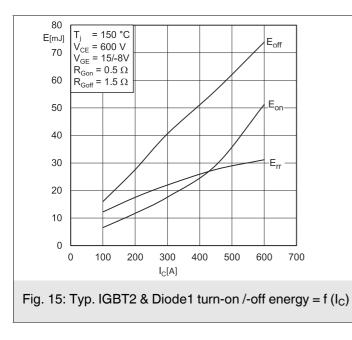


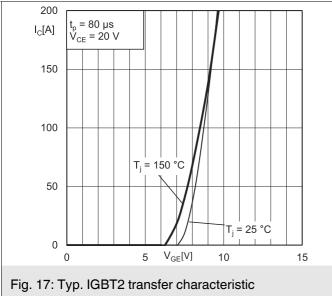


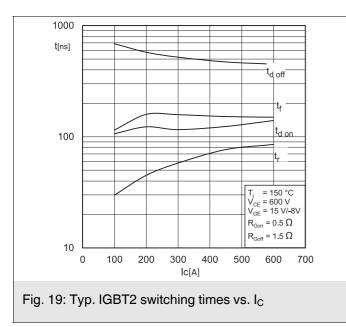


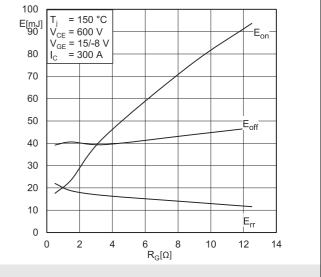


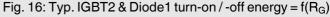
Rev. 0.10 - 12.07.2017











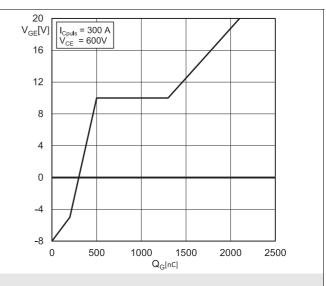
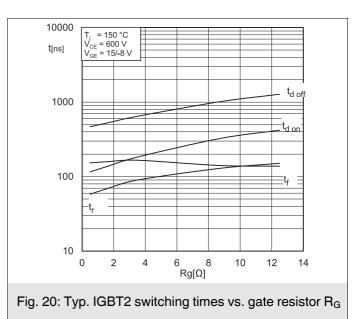
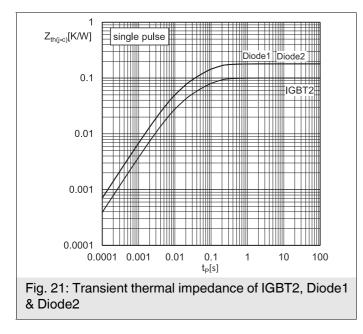
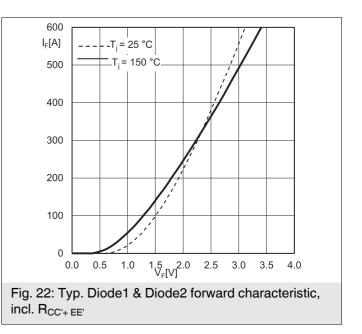
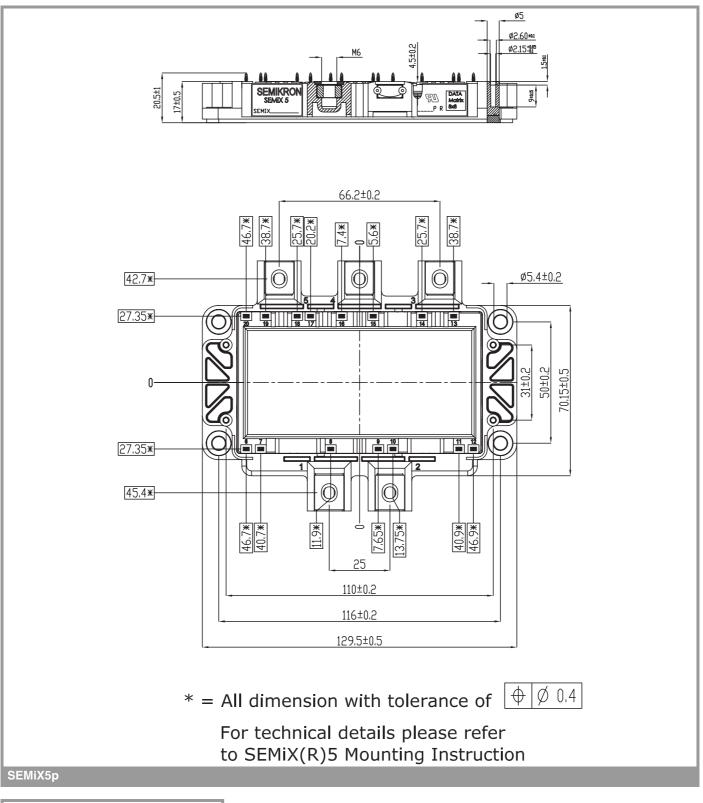


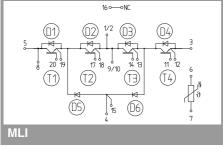
Fig. 18: Typ. IGBT2 gate charge characteristic











This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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